



STEVAL-IPE005V1, STPM01 programmer board - hardware

Introduction

The STPM01 - programmer kit works in conjunction with the STPM01 energy meter ASSP device and with the STPM01 manager software. It is an integrated system designed to provide the user with a complete, ready-to-use energy meter application. The reference design is a high-end solution for power metering based on the STR710 microcontroller with an embedded RTC and an external memory interface (EMI) ready to drive 1Mbyte of on-board SRAM.

The kit also integrates an on-board optical insulated serial line allowing isolation of the board ground reference in order to avoid propagation of overvoltage on the PC side.

Moreover, the on-board charge pump allows burning the STPM01 energy meter ASSP device registers.

Access to the STPM01 device registers is ensured using a dedicated SPI bus interface.

The STPM01 programmer kit demonstrates how effectively the STPM01 can be used in real-world energy meter applications and it helps the user to develop his own application.

The STPM01 programmer kit can be used in two ways:

- For demonstration purposes, by connecting the reference design to an AC power source and changing all the settings parameters through the GUI interface and the hardware programmer/reader board
- To evaluate and develop a custom application

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1 Overview

1.1 Recommended reading

This documentation describes how to configure and use the STPM01 programmer kit hardware. Additional information can be found in the following documents:

- Datasheets of ST devices
- Datasheets of third party devices
- Dedicated application notes
- STPM01 programmer firmware user manual
- STPowerMeter user manual

1.2 Getting technical support

Technical assistance is provided free to all customers. For technical assistance, documentation, information and upgrades about products and services, please refer to your local ST distributor/office.

STMicroelectronics offers its customers a free technical support service at metering@st.com and on www.st.com/meteringsupport.

2 STPM01 kit components

2.1 Package checklist

The STPM01 kit package includes the following items:

- The STPM01 board
- A CD-ROM with software and documentation

2.2 Debug

Software debug is via a standard 20-pin JTAG connection.

2.3 Reset

The reset sources are:

- Power-on reset
- Push-button reset
- JTAG reset from an in-circuit emulator

2.4 Power supplies

Power to the board is supplied using a lump in cord power supply providing 5 V to the board. All other required voltages are provided by on-board voltage regulators or voltage convectors.

An isolated 5 V DC/DC provides the isolated voltage supply for the opto-coupler devices.

2.5 Opto-isolated RS-232

A general-purpose, asynchronous serial I/O data port is connected through 9-pin D-type male connectors. The port is opto-isolated in order to avoid overvoltage on the PC side.

2.6 SPI connector

A devoted SPI connector allows interfacing the STPM01 device. It provides all the SPI signals required to communicate with the STPM01 interface registers. Moreover it provides a 24 V line which allows burning the STPM01 configuration registers.

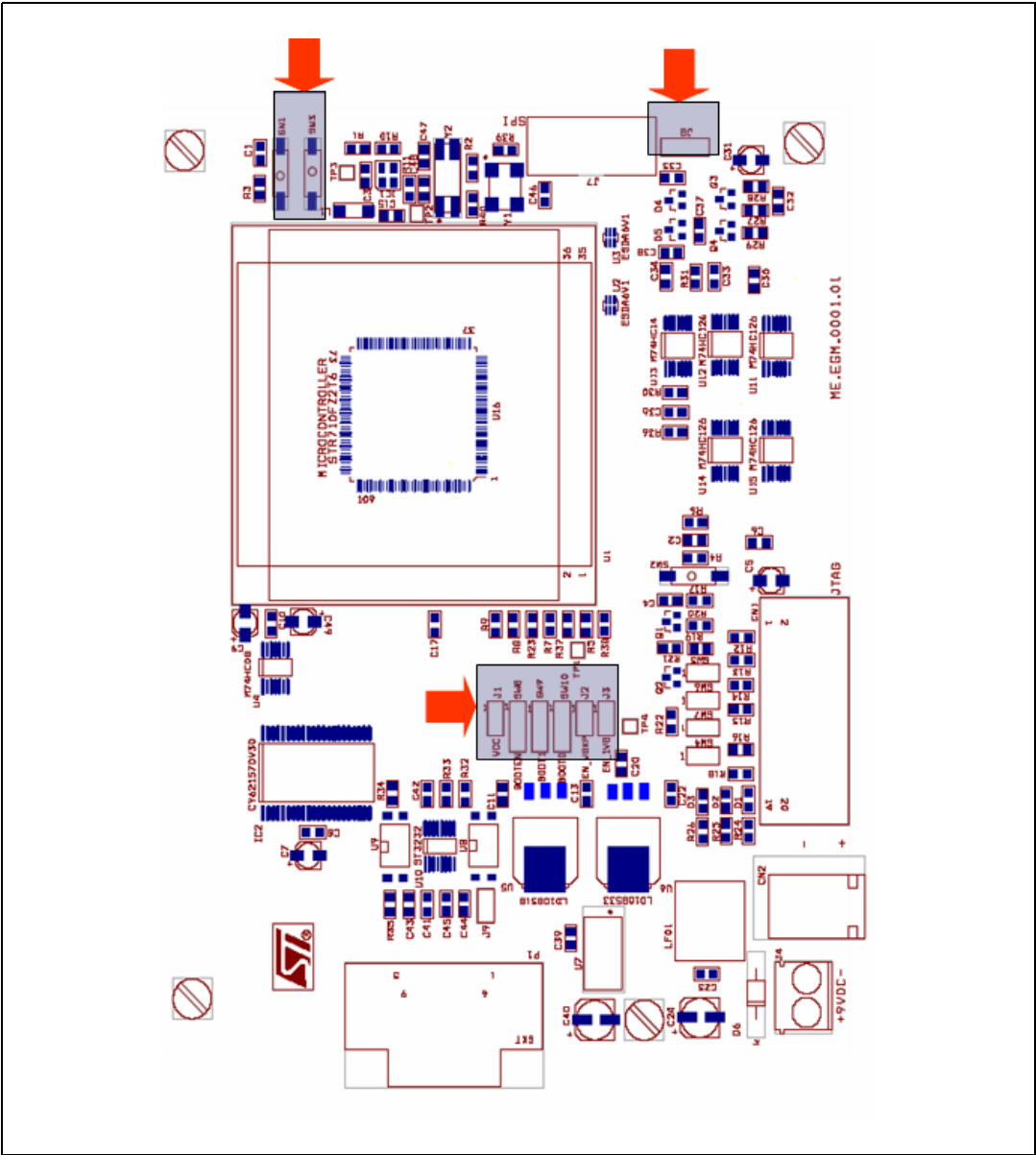
2.7 Status LEDs

Table 1. Status LEDs

LED	Description
D1	+5 V power
D2	+3.3 V power
D3	1.8 V power

2.8 Jumper placement

Figure 1. STPM01 programmer kit option jumper placement



2.9 Jumper position

Figure 2. Jumper position

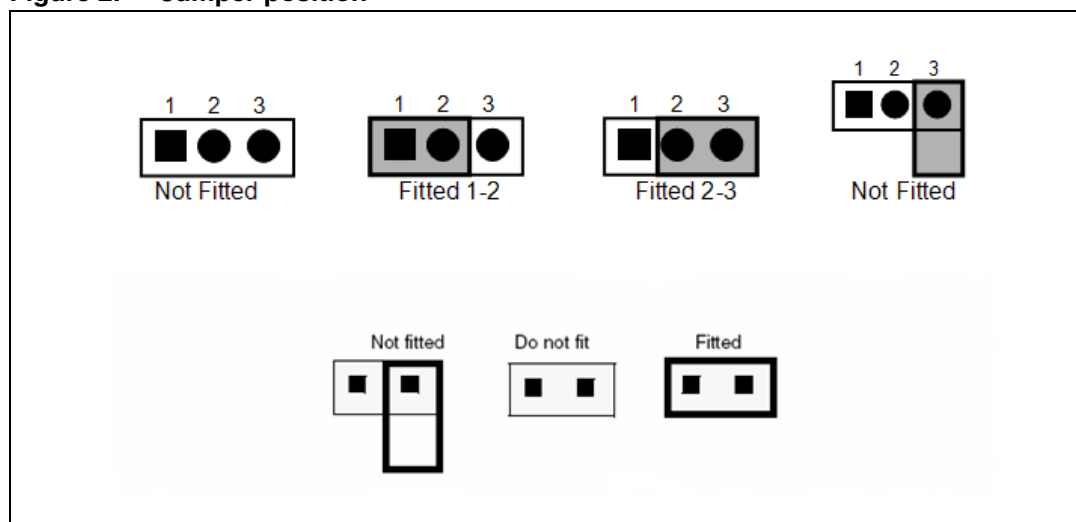


Table 2. Jumpers (refer to [Figure 1](#))

Jumper	Description	Default
J1	3.3 V power supply for external SRAM – Fitted: power to the SRAM – Not fitted: no power to the SRAM	Fitted
J2	Enable VBKP (stabilization for low power voltage) – Fitted: stabilization enabled – Not fitted: stabilization disabled	Not fitted
J3	Enable 1.8 V to microcontroller – Fitted: 1.8 V to microcontroller enabled – Not fitted: 1.8 V to microcontroller disabled	Fitted
J8	Power supply to the charge pump: – Fitted (1-2): Internal power supply to charge pump and SPI signals buffer – Fitted (2-3): power supply to charge pump and SPI signals buffer from STPM01 board	Fitted (1-2)
SW8	BOOT EN: – Fitted (1-2): GND – Fitted (2-3): 3.3 V	Not fitted
SW9	BOOT1: – Fitted (1-2): 3.3 V – Fitted (2-3): GND	Not fitted
SW10	BOOT0: – Fitted (1-2): 3.3 V – Fitted (2-3): GND	Not fitted

2.10 Description of push-buttons and default value

Table 3. Push-buttons (refer to [Figure 1](#))

Jumper	Description	Default
SW1	Wakeup: – When pushed, wakes up microcontroller	
SW2	Application default: – When pushed, forces default conditions	
SW3	Reset button: – When pushed, forces microcontroller reset	Fitted

2.11 Description of connectors

Figure 3. STPM01 programmer kit: placement of connectors

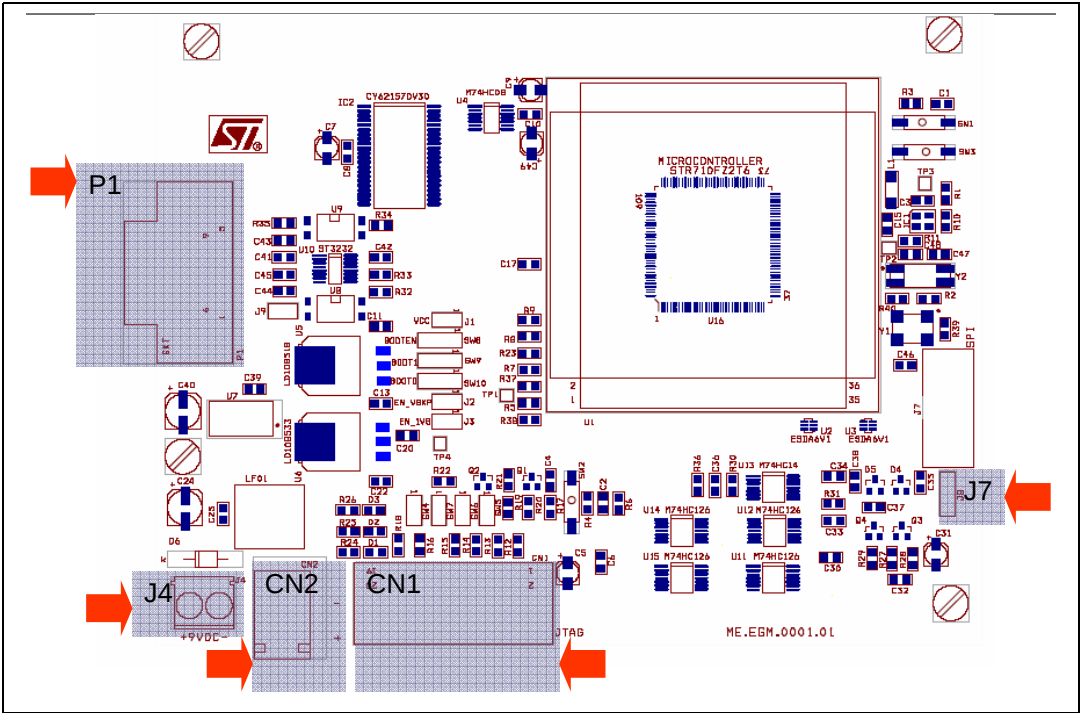


Table 4. Connectors (refer to [Figure 3](#))

Connector	Description
P1	Opto - isolated RS232 female connector
J4	DC-IN 5 V connector
CN2	DC-IN 5 V connector
CN1	JTAG connector
J7	SPI connector

2.12 Boot configuration

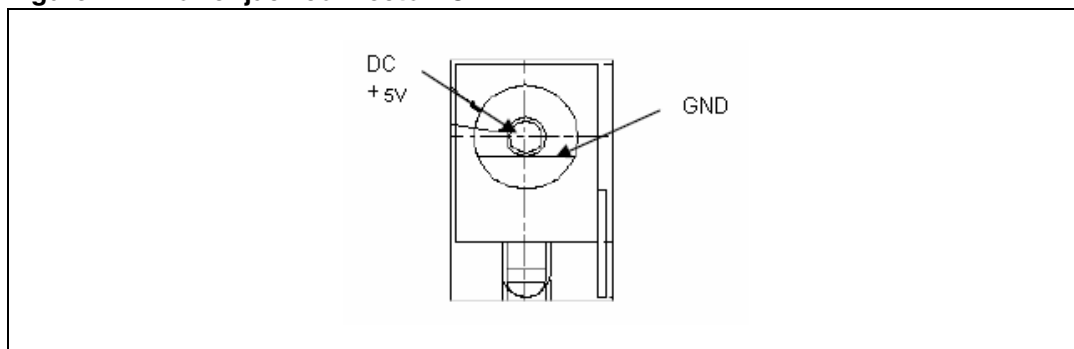
Table 5. Boot mode

BOOT EN SW8	BOOT1 SW9	BOOT0 SW10	Mode	BOOT memory mapping	Note
0 (1-2))	Any	Any	User	Flash mapped at 0h	System executes code from Flash
1 (2-3)	0 (2-3)	0 (2-3)			
1 (2-3)	0 (2-3)	1 (1-2)	System memory	System memory mapped at 0h	– System executes a factory installed boot loader from system memory (reserved mode) – Clock frozen
1 (2-3)	1 (1-2)	0 (2-3)	RAM	RAM mapped at 0h	– System executes code from internal RAM – For lab development
1 (2-3)	1 (1-2)	1 (1-2)	EXTMEM	EXTMEM mapped at 0h	– System executes code from external memory – Not allowed

3 Connectors

3.1 Power

Figure 4. Power jack connector: CN2



3.2 UARTx

Figure 5. RS232 transmit and receive connectors

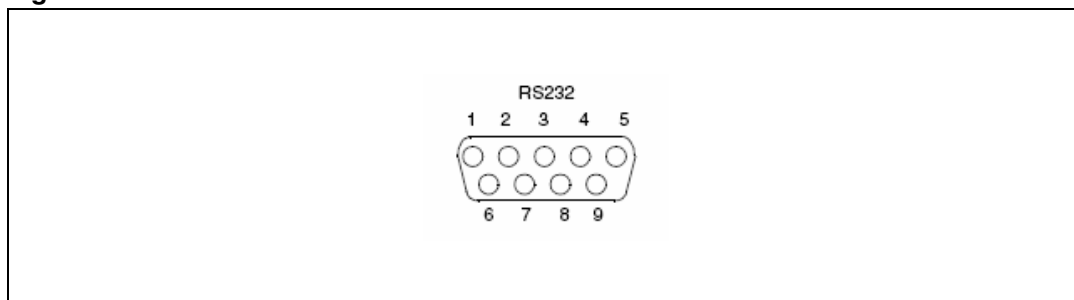


Table 6. RS232 connector pinout P1

Pin	Description
1	Shorted to pins 6 and 2
2	Shorted to pins 1 and 6
3	TX-0_PC (R1IN)
4	RX-0_PC (T1OUT)
5	GND
6	Shorted to pins 1 and 2
7	Shorted to pin 8
8	Shorted to pin 7
9	Not connected

3.3 Debug

Figure 6. JTAG standard interface: CN1

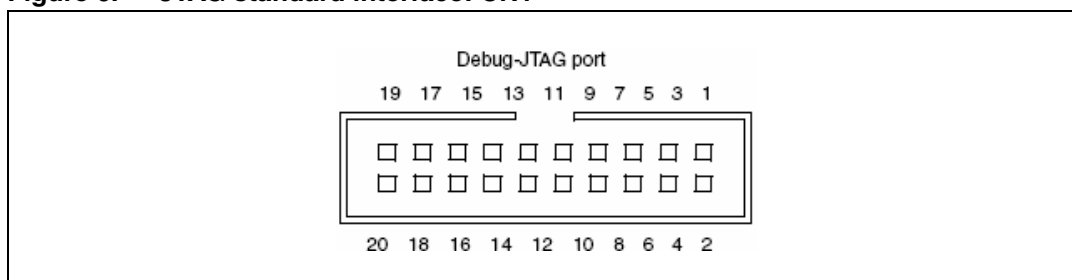
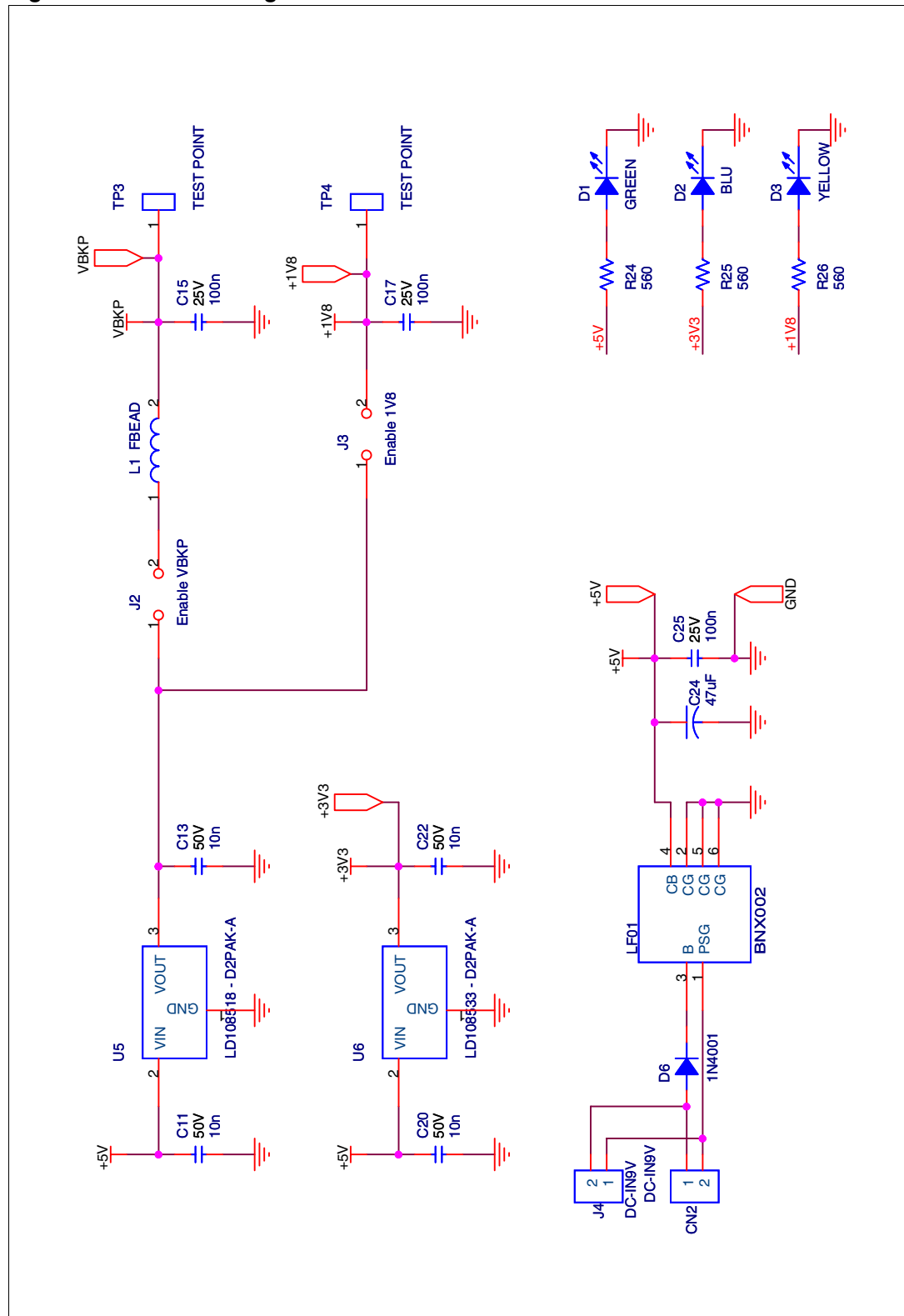


Table 7. JTAG interface pinout: CN10

Pin	Description
Even pins	Ground
1	VTref+3.3 V
2	Vsupply+3.3 V
3	notTRST
5	TDI
7	TMS
9	TCK
11	RTCK (ground)
13	TDo
15	noTReset
17	DBGRRQS - pulled down
19	Pulled down

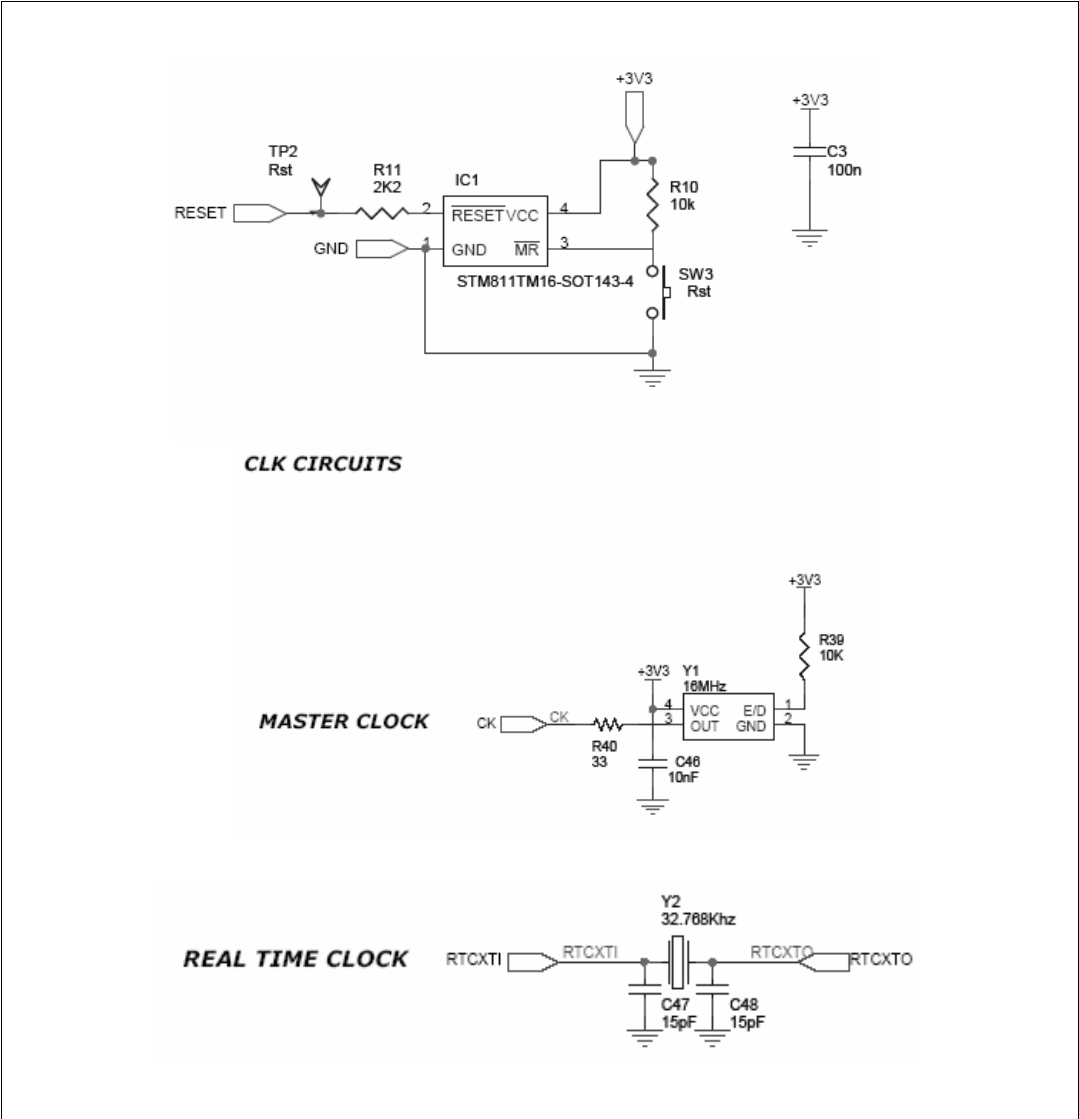
4.2 Power management

Figure 8. Power management schematic



4.3 Reset and clock circuits

Figure 9. Reset and clock circuits



4.4 Boot management and Jtag circuit

Figure 10. Boot management and Jtag circuits

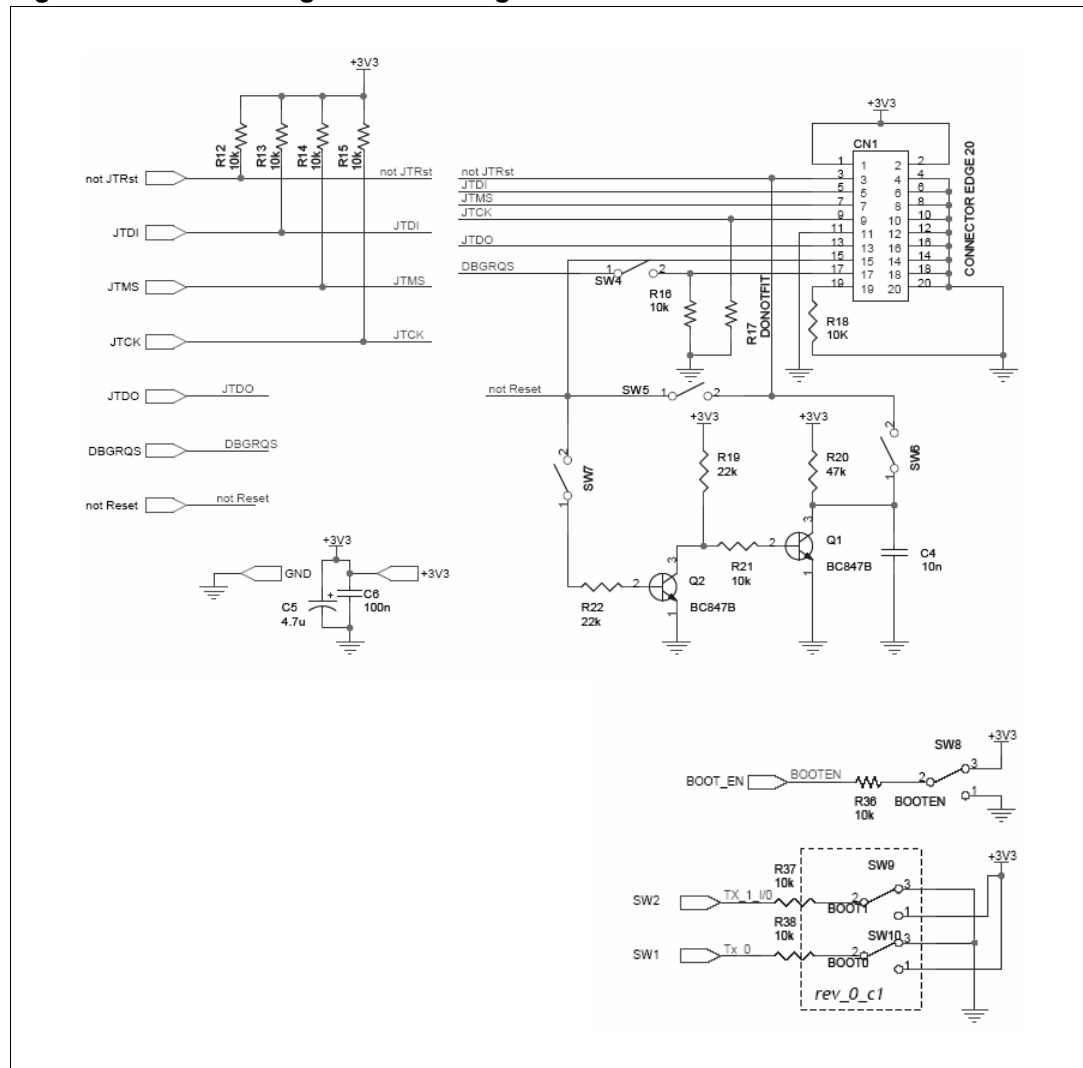
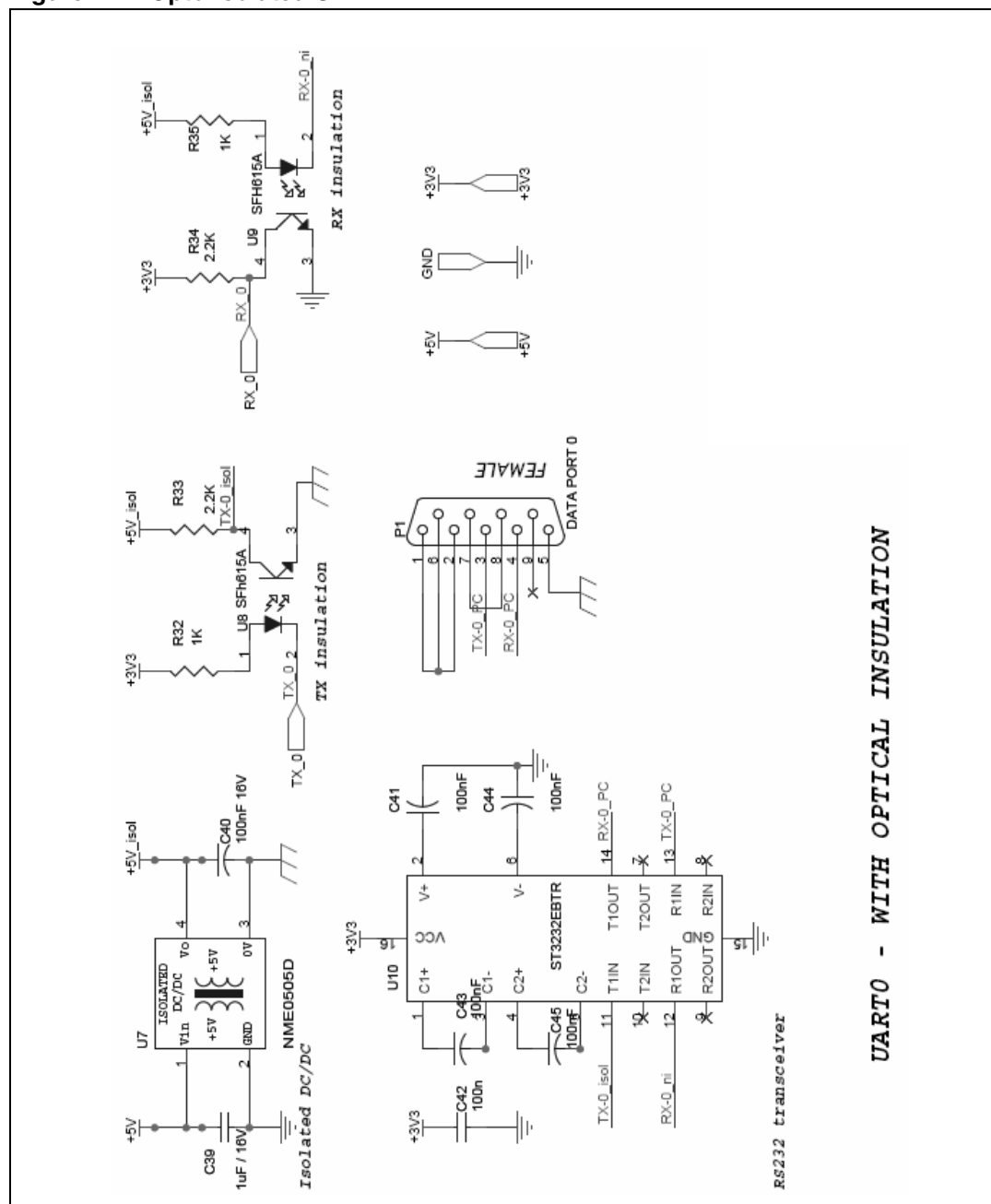
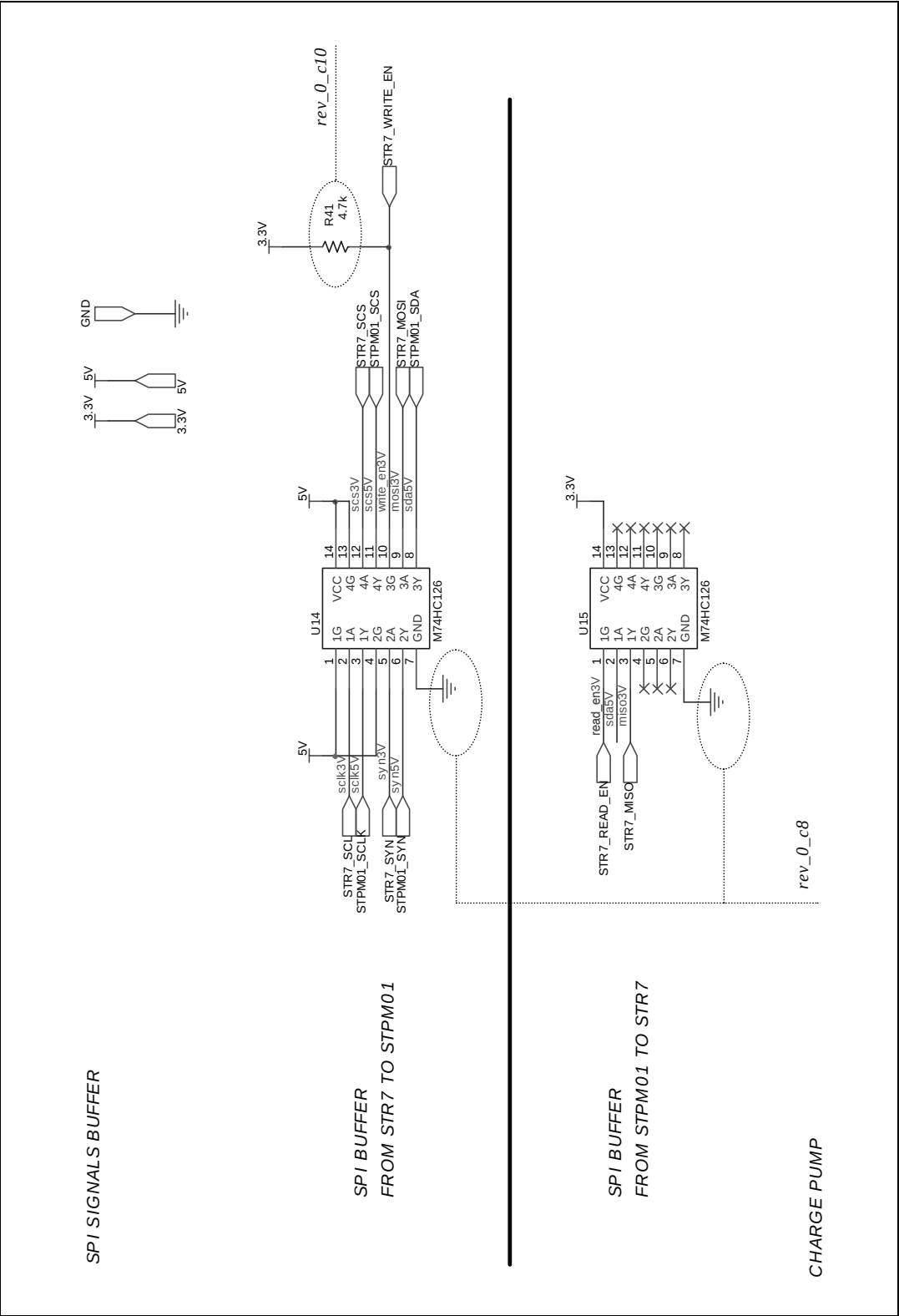


Figure 11. Opto-isolated UART



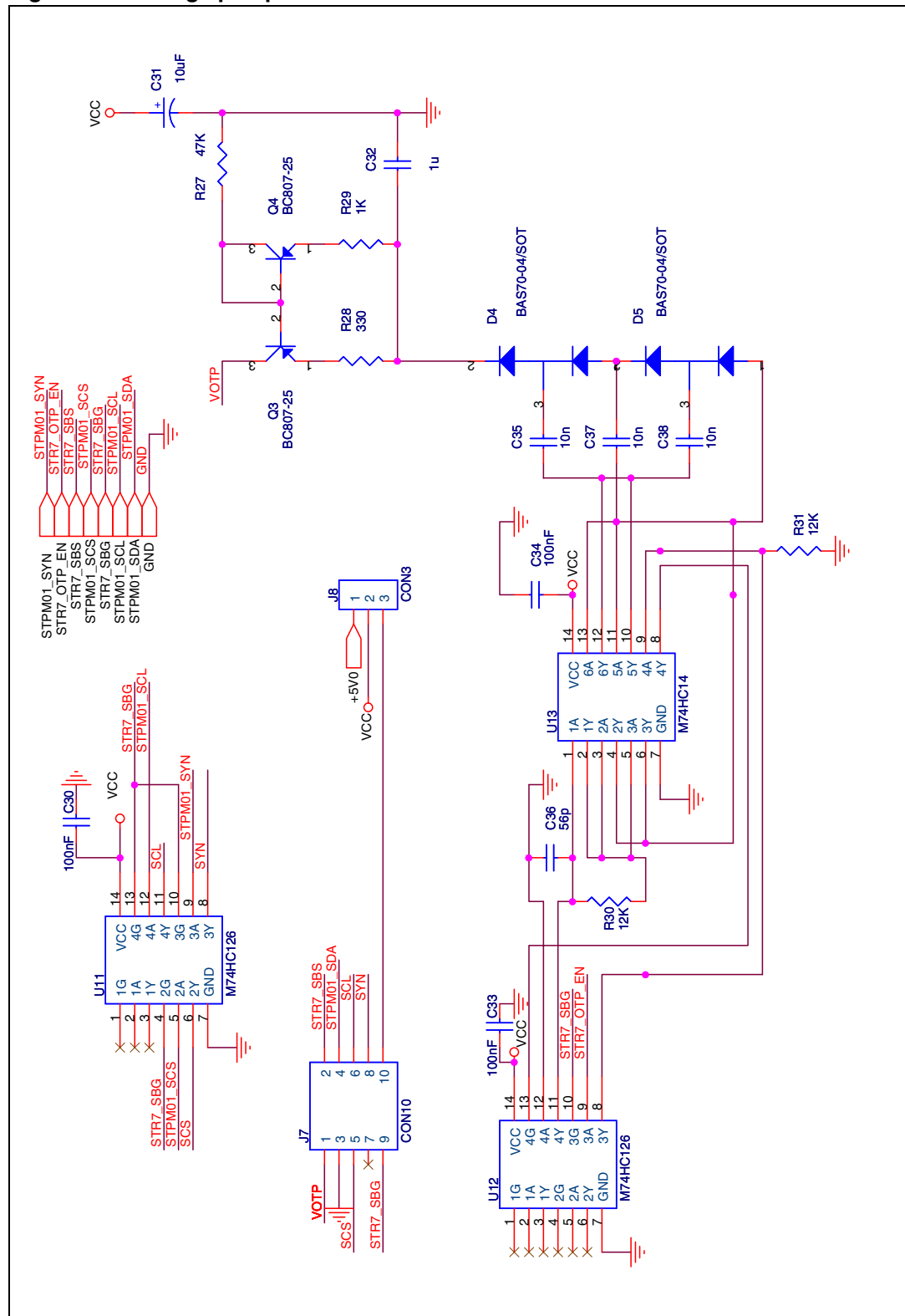
4.6 SPI signals buffering

Figure 13. SPI signals buffering



4.7 Charge pump and SPI connector

Figure 14. Charge pump and SPI connector



5 Revision history

Table 8. Document revision history

Date	Revision	Changes
14-Mar-2008	1	Initial release

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